

1st Aug, 2017

**KRISHNA ENGINEERING COLLEGE**

**ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT**

**GATE CLASSES**

(For 4<sup>th</sup> Yr Students)

**.Objective-**

- To provide coaching facilities, testing and evaluation to enable the aspirants to succeed in various competitive examinations for recruitment to services under the central and state governments, public and private undertakings etc.
- To keep ready for Entrance test for admissions into ME/ M. Tech and also for recruitment of candidates for jobs in PSUs.
- To provide study material for GATE exam preparation.

**Schedule**

<b>Date of Commencement:</b>	From 3rd Aug, 2017
<b>Timings:</b>	2:00pm to 4.30 pm (adjustable)
<b>Registration Date:</b>	Till 27 <sup>th</sup> July 2017.
<b>Subjects to be covered:</b>	1. Signals & Systems 2. Network Analysis & Synthesis 3. EMFT 4. Digital Electronics 5. Microprocessors 6. Control System 7. Analog Electronics 8. Communication System
<b>Coordinators:</b>	Mr Saurabh Singh (Asst. Prof., ECE) Mr Deepak Gupta (Asst. Prof., ECE)

**Copy to:**  
**Director- for kind information**  
**Dean (A)**  
**Notice Board ECE**

**Prof.(Dr.) A N Mishra**  
**HOD, ECE**

## List of Students

<b>S. No.</b>	<b>Name of Students</b>	<b>Section</b>
<b>1</b>	<b>Sudhir Kr Mishra</b>	<b>B</b>
<b>2</b>	<b>Ashish Sharma</b>	<b>A</b>
<b>3</b>	<b>Utkarsh Shukla</b>	<b>B</b>
<b>4</b>	<b>Hemlata</b>	<b>A</b>
<b>5</b>	<b>Jatin Pant</b>	<b>A</b>
<b>6</b>	<b>Babita Garg</b>	<b>A</b>
<b>7</b>	<b>Dhananjay singh</b>	<b>A</b>
<b>8</b>	<b>Jatin rana</b>	<b>A</b>
<b>9</b>	<b>Rajnish kr. singh</b>	<b>B</b>
<b>10</b>	<b>Sunney gupta</b>	<b>B</b>
<b>11</b>	<b>Ujjawal vats</b>	<b>B</b>
<b>12</b>	<b>Mohit kr.</b>	<b>B</b>
<b>13</b>	<b>Akshay sharma</b>	<b>A</b>
<b>14</b>	<b>Varun kumar</b>	<b>B</b>
<b>15</b>	<b>Saurabh Shukla</b>	<b>B</b>